

Electric Circuits By James Kang Hardcover Book

With vastly increased complexity and functionality in the "nanometer era" (i.e. hundreds of millions of transistors on one chip), increasing the performance of integrated circuits has become a challenging task. Connecting effectively (interconnect design) all of these chip elements has become the greatest determining factor in overall performance. 3-D integrated circuit design may offer the best solutions in the near future. This is the first book on 3-D integrated circuit design, covering all of the technological and design aspects of this emerging design paradigm, while proposing effective solutions to specific challenging problems concerning the design of 3-D integrated circuits. A handy, comprehensive reference or a practical design guide, this book provides a sound foundation for the design of 3-D integrated circuits. * Demonstrates how to overcome "interconnect bottleneck" with 3-D integrated circuit design...leading edge design techniques offer solutions to problems (performance/power consumption/price) faced by all circuit designers * The FIRST book on 3-D integrated circuit design...provides up-to-date information that is otherwise difficult to find * Focuses on design issues key to the product development cycle...good design plays a major role in exploiting the implementation flexibilities offered in the 3-D * Provides broad coverage of 3-D integrated circuit design, including interconnect prediction models, thermal management techniques, and timing optimization...offers practical view of designing 3-D circuits

Faculties, publications and doctoral theses in departments or divisions of chemistry, chemical engineering, biochemistry and pharmaceutical and/or medicinal chemistry at universities in the United States and Canada.

This Special Edition of Energies on "Energy Storage and Management for Electric Vehicles" draws together a collection of research papers that critically evaluates key areas of innovation and novelty when designing and managing the high-voltage battery system within an electrified powertrain. The addressed topics include design optimisation, mathematical modelling, control engineering, thermal management, and component sizing.

This book introduces readers to the fundamentals of deep neural network architectures, with a special emphasis on memristor circuits and systems. At first, the book offers an overview of neuro-memristive systems, including memristor devices, models, and theory, as well as an introduction to deep learning neural networks such as multi-layer networks, convolution neural networks, hierarchical temporal memory, and long short term memories, and deep neuro-fuzzy networks. It then focuses on the design of these neural networks using memristor crossbar architectures in detail. The book integrates the theory with various applications of neuro-memristive circuits and systems. It provides an introductory tutorial on a range of issues in the design, evaluation techniques, and implementations of different deep neural network architectures with memristors. Digital Logic with an Introduction to Verilog and FPGA-Based Design provides

basic knowledge of field programmable gate array (FPGA) design and implementation using Verilog, a hardware description language (HDL) commonly used in the design and verification of digital circuits. Emphasizing fundamental principles, this student-friendly textbook is an ideal resource for introductory digital logic courses. Chapters offer clear explanations of key concepts and step-by-step procedures that illustrate the real-world application of FPGA-based design. Designed for beginning students familiar with DC circuits and the C programming language, the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors. Subsequent chapters cover device level and logic level design in detail, including combinational and sequential circuits used in the design of microcontrollers and microprocessors. Topics include Boolean algebra and functions, analysis and design of sequential circuits using logic gates, FPGA-based implementation using CAD software tools, and combinational logic design using various HDLs with focus on Verilog.

This book covers a range of models, circuits and systems built with memristor devices and networks in applications to neural networks. It is divided into three parts: (1) Devices, (2) Models and (3) Applications. The resistive switching property is an important aspect of the memristors, and there are several designs of this discussed in this book, such as in metal oxide/organic semiconductor nonvolatile memories, nanoscale switching and degradation of resistive random access memory and graphene oxide-based memristor. The modelling of the memristors is required to ensure that the devices can be put to use and improve emerging application. In this book, various memristor models are discussed, from a mathematical framework to implementations in SPICE and verilog, that will be useful for the practitioners and researchers to get a grounding on the topic. The applications of the memristor models in various neuromorphic networks are discussed covering various neural network models, implementations in A/D converter and hierarchical temporal memories.

Arousal in Neurological and Psychiatric Diseases focuses on the dysregulation of arousal found in many neurological and psychiatric disorders. Chapters describe the physiology of each process, how it presents in each disorder, and the most appropriate treatment(s). The book also imparts the understanding of the RAS as a system that not only modulates waking, but also survival mechanisms, such as fight vs. flight responses and other reflexes. This book helps neuroscientists, sleep researchers, neurologists and psychiatrists understand the basic mechanisms that modulate arousal in health and disease. In addition, it promotes therapies that can alter the severity and manifestation of multiple disorders.

Provides a comprehensive overview of the basic mechanisms behind dysregulation of arousal in neurological and psychiatric disorders Describes, in detail, the function of the Reticular Activating System with respect to higher functions, motor control and the intertwining of arousal and motor disorders Covers multiple neurological disorders, including epilepsy, Alzheimer's disease,

Parkinson's disease and autism

Now readers can master the fundamentals of electric circuits with Kang's **ELECTRIC CIRCUITS**. Readers learn the basics of electric circuits with common design practices and simulations as the book presents clear step-by-step examples, practical exercises, and problems. Each chapter includes several examples and problems related to circuit design, with answers for odd-numbered questions so learners can further prepare themselves with self-guided study and practice. **ELECTRIC CIRCUITS** covers everything from DC circuits and AC circuits to Laplace transformed circuits. MATLAB scripts for certain examples give readers an alternate method to solve circuit problems, check answers, and reduce laborious derivations and calculations. This edition also provides PSpice and Simulink examples to demonstrate electric circuit simulations. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

Chapters in this manual are arranged to match the topics covered in the text. Each chapter introduces device definitions and/or PSpice commands along with examples.

Nanoelectronic Device Applications Handbook gives a comprehensive snapshot of the state of the art in nanodevices for nanoelectronics applications. Combining breadth and depth, the book includes 68 chapters on topics that range from nano-scaled complementary metal-oxide-semiconductor (CMOS) devices through recent developments in nano capacitors and AlGaAs/GaAs devices. The contributors are world-renowned experts from academia and industry from around the globe. The handbook explores current research into potentially disruptive technologies for a post-CMOS world. These include: Nanoscale advances in current MOSFET/CMOS technology Nano capacitors for applications such as electronics packaging and humidity sensors Single electron transistors and other electron tunneling devices Quantum cellular automata and nanomagnetic logic Memristors as switching devices and for memory Graphene preparation, properties, and devices Carbon nanotubes (CNTs), both single CNT and random network Other CNT applications such as terahertz, sensors, interconnects, and capacitors Nano system architectures for reliability Nanowire device fabrication and applications Nanowire transistors Nanodevices for spintronics The book closes with a call for a new generation of simulation tools to handle nanoscale mechanisms in realistic nanodevice geometries. This timely handbook offers a wealth of insights into the application of nanoelectronics. It is an invaluable reference and source of ideas for anyone working in the rapidly expanding field of nanoelectronics.

Microelectronic Circuit Design is known for being a technically excellent text. The new edition has been revised to make the material more motivating and accessible to students while retaining a student-friendly approach. Jaeger has added more pedagogy and an emphasis on design through the use of design examples and design notes. Some pedagogical elements include chapter

opening vignettes, chapter objectives, "Electronics in Action" boxes, a problem solving methodology, and "design note" boxes. The number of examples, including new design examples, has been increased, giving students more opportunity to see problems worked out. Additionally, some of the less fundamental mathematical material has been moved to the ARIS website. In addition this edition comes with a Homework Management System called ARIS, which includes 450 static problems.

After an overview of major scientific discoveries of the 18th and 19th centuries, which created electrical science as we know and understand it and led to its useful applications in energy conversion, transmission, manufacturing industry and communications, this Circuits and Systems History book fills a gap in published literature by providing a record of the many outstanding scientists, mathematicians and engineers who laid the foundations of Circuit Theory and Filter Design from the mid-20th Century. Additionally, the book records the history of the IEEE Circuits and Systems Society from its origins as the small Circuit Theory Group of the Institute of Radio Engineers (IRE), which merged with the American Institute of Electrical Engineers (AIEE) to form IEEE in 1963, to the large and broad-coverage worldwide IEEE Society which it is today. Many authors from many countries contributed to the creation of this book, working to a very tight time-schedule. The result is a substantial contribution to their enthusiasm and expertise which it is hoped that readers will find both interesting and useful. It is sure that in such a book omissions will be found and in the space and time available, much valuable material had to be left out. It is hoped that this book will stimulate an interest in the marvellous heritage and contributions that have come from the many outstanding people who worked in the Circuits and Systems area.

This book presents a comprehensive overview of nanoscale electronics and systems packaging, and covers nanoscale structures, nanoelectronics packaging, nanowire applications in packaging, and offers a roadmap for future trends. Composite materials are studied for high-k dielectrics, resistors and inductors, electrically conductive adhesives, conductive "inks," underfill fillers, and solder enhancement. The book is intended for industrial and academic researchers, industrial electronics packaging engineers who need to keep abreast of progress in their field, and others with interests in nanotechnology. It surveys the application of nanotechnologies to electronics packaging, as represented by current research across the field.

Illustrates the use of several modeling and simulation techniques for optoelectronic circuit and system design analysis, intended to address the general lack of advanced modeling and simulation infrastructure currently available. Topics range from a review of conventional electronic circuit simulati

DIGITAL LOGIC AND MICROPROCESSOR DESIGN WITH INTERFACING, 2E provides a solid foundation for designing digital logic circuits. This unique approach combines the use of logic principles and the building of individual components to create data paths and control units so readers can build dedicated custom microprocessors and general-purpose microprocessors. Readers design simple microprocessors from

the ground up, implement them in real hardware, and interface them to actual devices. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

This exciting new text teaches the foundations of electric circuits and develops a thinking style and a problem-solving methodology that is based on physical insight. Designed for the first course or sequence in circuits in electrical engineering, the approach imparts not only an appreciation for the elegance of the mathematics of circuit theory, but a genuine "feel" for a circuit's physical operation. This will benefit students not only in the rest of the curriculum, but in being able to cope with the rapidly changing technology they will face on-the-job. The text covers all the traditional topics in a way that holds students' interest. The presentation is only as mathematically rigorous as is needed, and theory is always related to real-life situations. Franco introduces ideal transformers and amplifiers early on to stimulate student interest by giving a taste of actual engineering practice. This is followed by extensive coverage of the operational amplifier to provide a practical illustration of abstract but fundamental concepts such as impedance transformation and root location control--always with a vigilant eye on the underlying physical basis. SPICE is referred to throughout the text as a means for checking the results of hand calculations, and in separate end-of-chapter sections, which introduce the most important SPICE features at the specific points in the presentation at which students will find them most useful. Over 350 worked examples, 400-plus exercises, and 1000 end-of-chapter problems help students develop an engineering approach to problem solving based on conceptual understanding and physical intuition rather than on rote procedures.

This book is a very timely exposition of part of an important subject which goes under the general name of "inverse problems". The analogous problem for continuous media has been very much studied, with a great deal of difficult mathematics involved, especially partial differential equations. Some of the researchers working on the inverse conductivity problem for continuous media (the problem of recovering the conductivity inside from measurements on the outside) have taken an interest in the authors' analysis of this similar problem for resistor networks. The authors' treatment of inverse problems for electrical networks is at a fairly elementary level. It is accessible to advanced undergraduates, and mathematics students at the graduate level. The topics are of interest to mathematicians working on inverse problems, and possibly to electrical engineers. A few techniques from other areas of mathematics have been brought together in the treatment. It is this amalgamation of such topics as graph theory, medial graphs and matrix algebra, as well as the analogy to inverse problems for partial differential equations, that makes the book both original and interesting.

Contents: Circular Planar Graphs Resistor Networks Harmonic Functions Characterization I Adjoining Edges Characterization II Medial Graphs Recovering a Graph Layered Networks Readership: Graduate students and researchers in applied mathematics and electrical and electronic engineering. Keywords: Inverse Problems; Resistor Network; Schur Complement; Medial Graph; Circular Planar Graph; Kirchhoff Matrix; Response Matrix; Y - Δ Transformation; Gamma-Harmonic Function; Connections; Dirichlet Problem

Introduction to Circuit Analysis and Design takes the view that circuits have inputs and outputs, and that relations between inputs and outputs and the terminal characteristics

of circuits at input and output ports are all-important in analysis and design. Two-port models, input resistance, output impedance, gain, loading effects, and frequency response are treated in more depth than is traditional. Due attention to these topics is essential preparation for design, provides useful preparation for subsequent courses in electronic devices and circuits, and eases the transition from circuits to systems. This book presents the combined proceedings of the 12th International Conference on Multimedia and Ubiquitous Engineering (MUE 2018) and the 13th International Conference on Future Information Technology (Future Tech 2018), both held in Salerno, Italy, April 23 - 25, 2018. The aim of these two meetings was to promote discussion and interaction among academics, researchers and professionals in the field of ubiquitous computing technologies. These proceedings reflect the state of the art in the development of computational methods, involving theory, algorithms, numerical simulation, error and uncertainty analysis and novel applications of new processing techniques in engineering, science, and other disciplines related to ubiquitous computing.

Emerging Nanoelectronic Devices focuses on the future direction of semiconductor and emerging nanoscale device technology. As the dimensional scaling of CMOS approaches its limits, alternate information processing devices and microarchitectures are being explored to sustain increasing functionality at decreasing cost into the indefinite future. This is driving new paradigms of information processing enabled by innovative new devices, circuits, and architectures, necessary to support an increasingly interconnected world through a rapidly evolving internet. This original title provides a fresh perspective on emerging research devices in 26 up to date chapters written by the leading researchers in their respective areas. It supplements and extends the work performed by the Emerging Research Devices working group of the International Technology Roadmap for Semiconductors (ITRS). Key features: Serves as an authoritative tutorial on innovative devices and architectures that populate the dynamic world of "Beyond CMOS" technologies. Provides a realistic assessment of the strengths, weaknesses and key unknowns associated with each technology. Suggests guidelines for the directions of future development of each technology. Emphasizes physical concepts over mathematical development. Provides an essential resource for students, researchers and practicing engineers.

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